

MBR1535CT, MBR1545CT

MBR1545CT is a Preferred Device

SWITCHMODE™ Power Rectifier

These state-of-the-art devices use the Schottky Barrier principle with a platinum barrier metal.

Features

- Center-Tap Configuration
- Guardring for Stress Protection
- Low Forward Voltage
- 150°C Operating Junction Temperature
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Pb-Free Packages are Available*

Mechanical Characteristics

- Case: Epoxy, Molded
- Weight: 1.9 grams (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped 50 units per plastic tube

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|---------------------------------|-------------|------------------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage MBR1535CT MBR1545CT | V_{RRM} V_{RWM} V_R | 35 45 | V |
| Average Rectified Forward Current (Rated V_R , $T_C = 105^\circ\text{C}$) Per Diode Per Device | $I_{F(AV)}$ | 7.5 15 | A |
| Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz, $T_C = 105^\circ\text{C}$) Per Diode | I_{FRM} | 15 | A |
| Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz) | I_{FSM} | 150 | A |
| Peak Repetitive Reverse Surge Current (2.0 μs , 1.0 kHz) | I_{RRM} | 1.0 | A |
| Storage Temperature Range | T_{stg} | -65 to +175 | °C |
| Operating Junction Temperature | T_J | -65 to +150 | °C |
| Voltage Rate of Change (Rated V_R) | dv/dt | 1000 | V/ μs |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

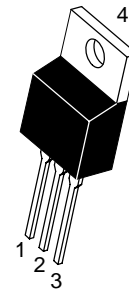
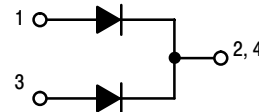
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

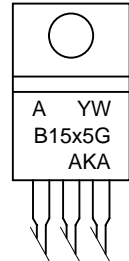
<http://onsemi.com>

SCHOTTKY BARRIER RECTIFIERS 15 AMPERES 35 and 45 VOLTS



TO-220AB
CASE 221A
PLASTIC

MARKING DIAGRAM



A = Assembly Location
Y = Year
W = Work Week
B15x5 = Device Code
x = 3 or 4
G = Pb-Free Package
AKA = Diode Polarity

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

MBR1535CT, MBR1545CT

THERMAL CHARACTERISTICS PER DIODE

| Characteristic | Symbol | Value | Unit |
|---|-----------------|-------|------|
| Maximum Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 3.0 | °C/W |
| Maximum Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 60 | °C/W |

ELECTRICAL CHARACTERISTICS PER DIODE

| Characteristic | Symbol | Value | Unit |
|--|--------|----------------------|------|
| Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 7.5$ Amps, $T_C = 125^\circ\text{C}$) ($i_F = 15$ Amps, $T_C = 125^\circ\text{C}$) ($i_F = 15$ Amps, $T_C = 25^\circ\text{C}$) | V_F | 0.57 0.72 0.84 | V |
| Maximum Instantaneous Reverse Current (Note 1) (Rated DC Voltage, $T_C = 125^\circ\text{C}$) (Rated DC Voltage, $T_C = 25^\circ\text{C}$) | i_R | 15 0.1 | mA |

1. Pulse Test: Pulse Width = 300 μs , Duty Cycle $\leq 2.0\%$

ORDERING INFORMATION

| Device | Package | Shipping† |
|------------|---------------------|-----------------|
| MBR1535CT | TO-220 | 50 Units / Rail |
| MBR1535CTG | TO-220 (Pb-Free) | 50 Units / Rail |
| MBR1545CT | TO-220 | 50 Units / Rail |
| MBR1545CTG | TO-220 (Pb-Free) | 50 Units / Rail |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MBR1535CT, MBR1545CT

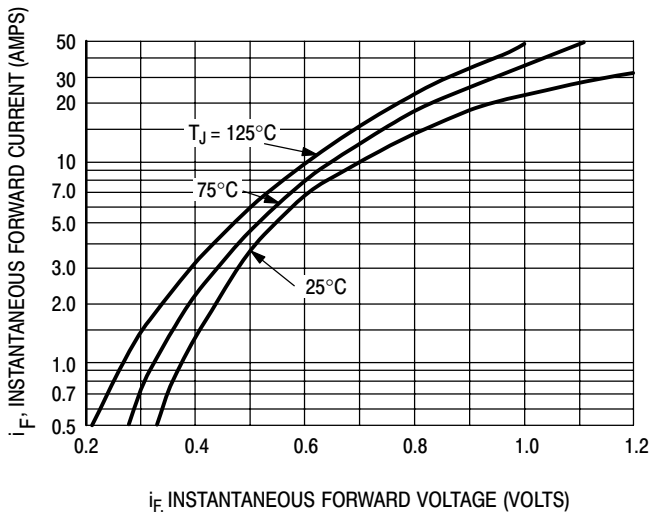


Figure 1. Typical Forward Voltage

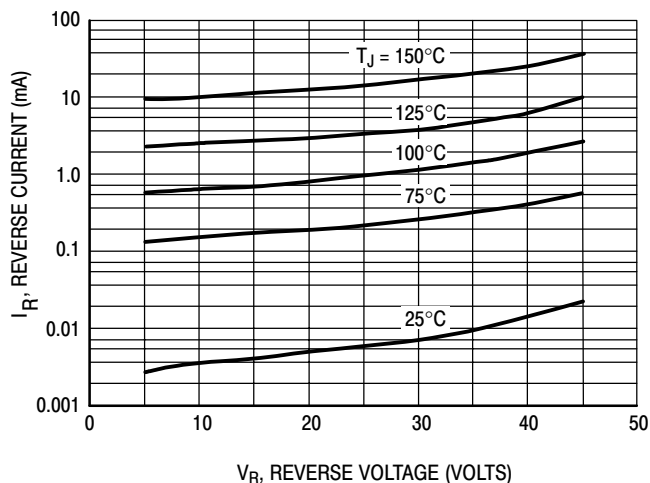


Figure 2. Typical Reverse Current

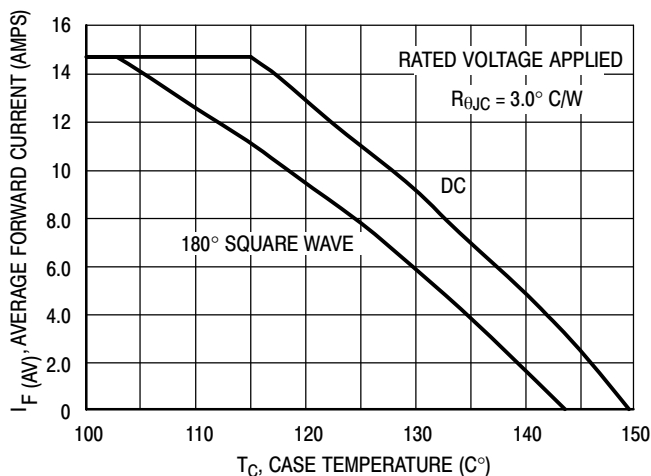


Figure 3. Current Derating, Case

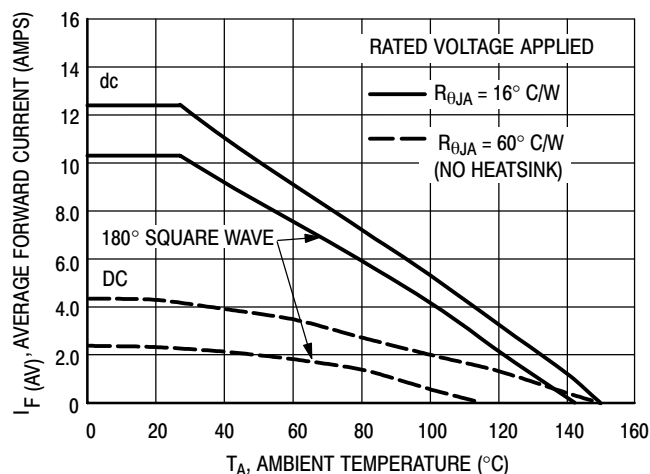


Figure 4. Current Derating, Ambient

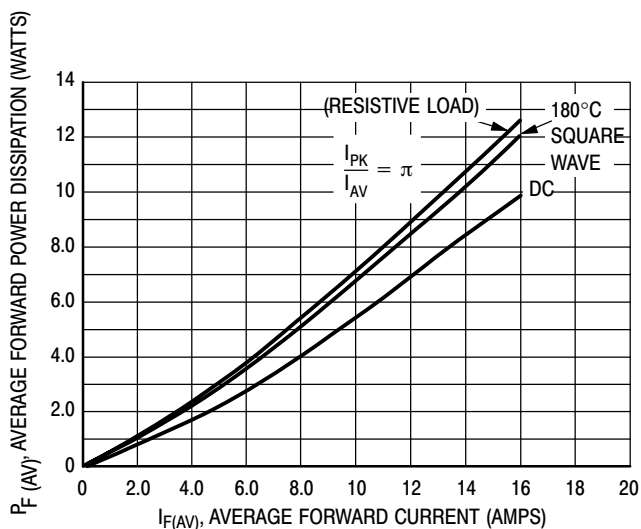
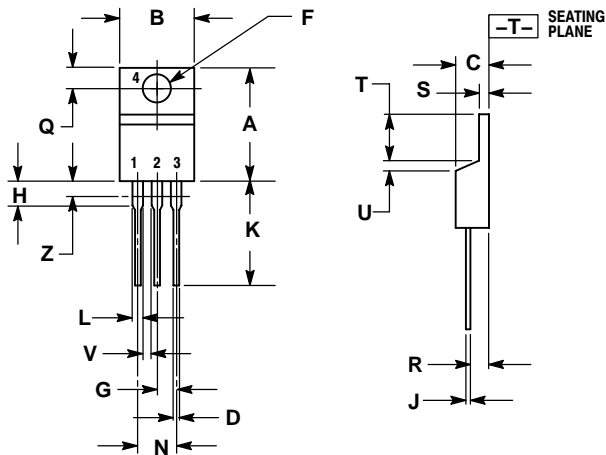


Figure 5. Power Dissipation

MBR1535CT, MBR1545CT

PACKAGE DIMENSIONS

TO-220
CASE 221A-09
ISSUE AA



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

| DIM | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.570 | 0.620 | 14.48 | 15.75 |
| B | 0.380 | 0.405 | 9.66 | 10.28 |
| C | 0.160 | 0.190 | 4.07 | 4.82 |
| D | 0.025 | 0.035 | 0.64 | 0.88 |
| F | 0.142 | 0.147 | 3.61 | 3.73 |
| G | 0.095 | 0.105 | 2.42 | 2.66 |
| H | 0.110 | 0.155 | 2.80 | 3.93 |
| J | 0.018 | 0.025 | 0.46 | 0.64 |
| K | 0.500 | 0.562 | 12.70 | 14.27 |
| L | 0.045 | 0.060 | 1.15 | 1.52 |
| N | 0.190 | 0.210 | 4.83 | 5.33 |
| Q | 0.100 | 0.120 | 2.54 | 3.04 |
| R | 0.080 | 0.110 | 2.04 | 2.79 |
| S | 0.045 | 0.055 | 1.15 | 1.39 |
| T | 0.235 | 0.255 | 5.97 | 6.47 |
| U | 0.000 | 0.050 | 0.00 | 1.27 |
| V | 0.045 | --- | 1.15 | --- |
| Z | --- | 0.080 | --- | 2.04 |

SWITCHMODE is a registered trademark of Semiconductor Components Industries, LLC (SCILLC).

ON Semiconductor and **ON** are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your local Sales Representative.